R. Allan Brett Reg. No. 40,476 Signature

Dated: January 2, 2007

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PATENT NO.

DOC Code: COCIN

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Page 1 of 1

APPLICATION NO. : 10/728,875

ISSUE DATE

: July 25, 2006

INVENTOR(S)

: Roger Maitland, et al

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

- 1) Column 9, line 48, "... TBs may be stored..." should read --... TBs may be stored...--:
- Column 10, line 64, "...RAMs is fficient." should read --...RAMs is efficient.--; 2)
- 3)
- Column 11, line 61, "...NDCH,..." should read "...N_{DCH},..."; Column 11, line 64, "...NDCH,..." should read --...N_{DCH},...-; 4)
- Column 12, line 1, "...NDCH, can be etermined..." should read "...NDCH, can be 5) determined...";
- Column 12, line 5, "...NDCH,..." should read -- NDCH,...-; 6)
- Column 17, line 48, "...FIG. 1C,..." should read --...FIG. 11C,...-; Column 18, line 12, "...FIG. 1C,..." should read --...FIG. 11C,...-; 7)
- 8)
- Column 23, line 50, "...t' start = $t_{center} S_{win}/2 + t_{offset}$..." should read --...t' start = 9) $t'_{center} - S_{win}/2 + t_{offset} - \cdots$
- Column 24, claim 1, line 60, "...with each group of corresponding payload 10) sequences..." should be --... from each at least one payload sequence, each...-;
- Column 26, claim 8, line 21, "...group or corresponding..." should read --11) ...group of corresponding...-.

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